

Docket No. 55709

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Maik BRETT; Manfred MENDE;

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Filed: Herewith

For: METHOD AND CIRCUIT ARRANGEMENT FOR PICTURE-IN-PICTURE
INSERTION

ASSISTANT COMMISSIONER OF PATENTS AND TRADEMARKS
WASHINGTON, D.C. 20231

Sir:

PRELIMINARY AMENDMENT

Applicants file herewith the above-referenced application. Please amend the application as follows.

IN THE CLAIMS

Please claims 1 through 13 without prejudice.

Please add the following new claims.

14. A method for picture-in-picture insertion,
wherein a sequence of insertion pictures ($K_j=K_1, K_2, \dots$) decimated by vertical decimation ($VD \geq 1$) are read into a memory device (S) and subsequently read out,
wherein the insertion pictures (K_j) read out are inserted into a sequence of main pictures ($H_i=H_1, H_2, \dots$),

wherein the memory device (S) has a storage capacity of less than two insertion pictures (K_j) and is subdivided into memory segments (X,Y,Z;A,B,C,D,E) which are continuously overwritten by the insertion pictures, and

wherein a decision is made as to whether the currently written insertion picture (K_j) or the immediately preceding insertion picture (K_{j-1}) is read out,

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cont.

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